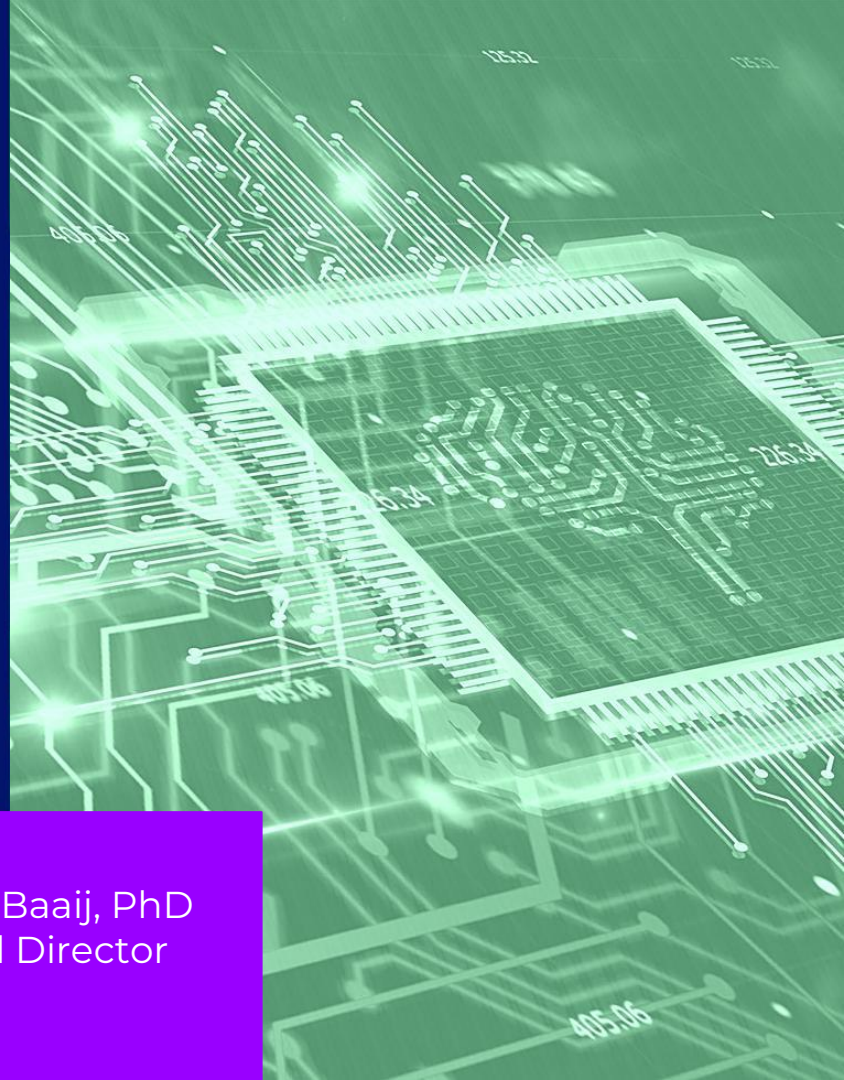




# QBayLogic

The key to FPGA and digital ASIC design

Christiaan Baaij, PhD  
Technical Director

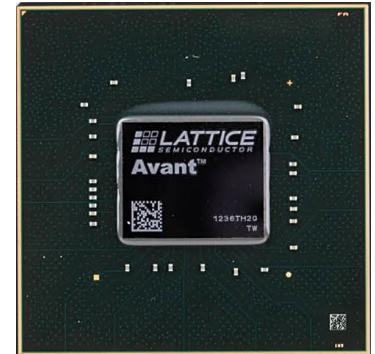
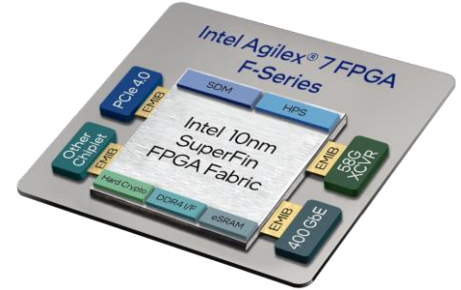


# About QBayLogic

Our mission:

Building *elegant* solutions together

- **FPGA** specialists
- Digital **ASIC** design partner in the Twente region, a region well regarded for its **microelectronics** design and research
- Stewards of the *open-source* **Clash** project:  
An elegant and functional approach to circuit design <https://clash-lang.org/>

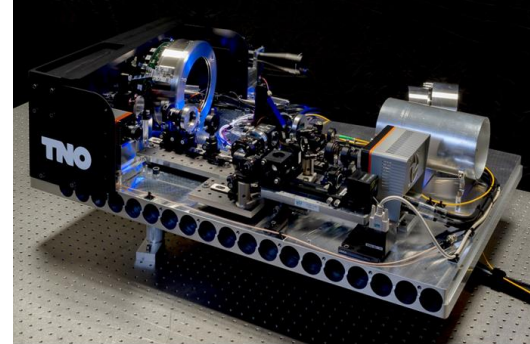
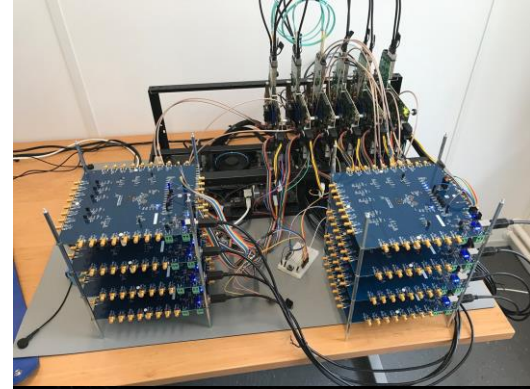


# Working together

Locally:



Internationally:



# Why we are here

QBayLogic's Big Hairy Audacious Goal:

**In 2030, 50% of all new FPGA projects are designed using an elegant functional method such as Clash**

- Japanese semiconductor companies are held in ***high esteem***
- Adoption of our method by even a single Japanese partner would have significant impact on the ***success*** of that goal

# Contact details

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